

10/049486

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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10049486	FILING DATE 02/28/2002	CLASS 251	SUBCLASS 604	GAU 2811	EXAMINER OWENS
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**APPLICANTS: Xu Yelin; Jiang Ling; Xu Qiang;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/CN00/00068 03/29/2000

** FOREIGN APPLICATIONS VERIFIED:

CHINA 99118929.9 08/30/1999

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO FP01074US
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials		
TITLE : Parallel plate diode		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Amount Due		Print Fig.	
Date Paid			
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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